

Docket No.: GR 98 P 2078

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RCE of Applic. No. : 09/124,288 Confirmation No.: 6541
Inventor : Jens Barrenscheen et al.
RCE Filed : December 11, 2003
TC/A.U. : 2155
Examiner : Patrice Winder

Docket No. : GR 98 P 2078
Customer No. : 24131

Hon. Commissioner for Patents
Alexandria, VA 22313-1450

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INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. 1.97(b)(4)

Sir:

In accordance with 37 C.F.R. 1.97(b)(4), copies of the following patents and/or publications are submitted herewith:

U.S. Patent No. 4,158,235 (Call et al.), dated June 12, 1979;

"Software-Controlled Wrap-Around Buffer", IBM Technical Disclosure Bulletin, Vol. 28, No. 12, May 1986, pp. 5314-16;

Tam-Anh Chu et al.: "Design of VLSI Asynchronous FIFO Queues for Packet Communication Networks", XP-000756967, IEEE, 1986, pp. 397-400;

S.G. Tzafestas: "On the Design of Multi-Input Periodic-Block-Output Buffers with Binomial Feedback", XP-002123624, pp. 120-125.

Respectfully submitted,

For Applicants
Date: December 11, 2003
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FORM PTO-1449 (SUBSTITUTE)

U.S. DEPARTMENT OF COMMERCE
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STATEMENT BY APPLICANT
(37 CFR 1.98(b))Attorney Docket No.:
GR 98 P 2078RCE of Applic. No.
09/124,288

Applicant

Jens Barrenscheen et al.

RCE Filing Date
December 11, 2003Group Art Unit
2155

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
	A	4,158,235	06/79	Call et al.			
	B						
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FOREIGN PATENT DOCUMENT

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	N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

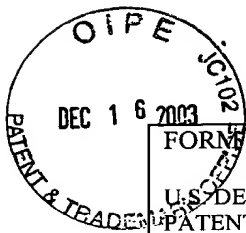
O	"Software-Controlled Wrap-Around Buffer", IBM Technical Disclosure Bulletin, Vol. 28, No. 12, May 1986, pp. 5314-16.
P	Tam-Anh Chu et al.: "Design of VLSI Asynchronous FIFO Queues for Packet Communication Networks", XP-000756967, IEEE, 1986, pp. 397-400.

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